

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Jeng H. Hwang et al.

§ GROUP ART UNIT: 1756

(Parent Application)

SERIAL NO.: 09/421,467

§ EXAMINER: N. Barreca

(Parent Application)

FILED: October 19, 1999

§ Attorney Docket No.:  
AM-2602.C1

FOR: MASKING METHODS AND ETCHING  
SEQUENCES FOR PATTERNING ELECTRODES  
OF HIGH DENSITY RAM CAPACITORS

Date: January 24, 2002

**PRELIMINARY AMENDMENT "A"**

**Hon. Commissioner for Patents  
Washington, DC 20231**

Sir:

This Preliminary Amendment "A" accompanies a continuation application of U.S. Application Serial No. 09/421,467, filed October 19, 1999.

**CERTIFICATE OF MAILING UNDER 37 CFR § 1.10**

I hereby certify that this paper and any documents said to accompany this paper are being deposited with the U.S. Postal Service on the date shown below with sufficient postage as U.S. EXPRESS MAIL NO. EU092830426US in an envelope addressed to the: Commissioner for Patents, Box Patent Application, Washington, DC 20231.

Date: January 24, 2002

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